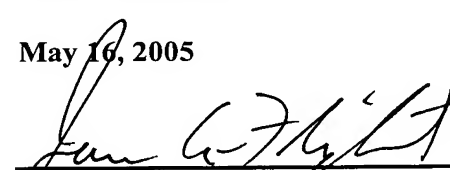




JPW  
B

PATENT  
Docket No. 20059/PIA31207

IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE

Applicant(s): Seo	)	I hereby certify that this paper is
	)	being deposited with the United
	)	States Postal Service with
Serial No.: 10/750,245	)	sufficient postage as first class
	)	mail in an envelope addressed to:
	)	Commissioner for Patents, P.O.
Filed: December 31, 2003	)	Box 1450, Alexandria, VA 22313-
	)	1450 on this date:
	)	
For: METHODS FOR FORMING A	)	May 16, 2005
GATE IN A SEMICONDUCTOR DEVICE	)	
	)	
	)	
Group Art Unit: 2818	)	
	)	James A. Flight
	)	Registration No. 37,622
Examiner: David Nhu	)	Attorney for Applicant(s)

**LETTER**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

The USPTO issued a notice of allowability on December 17, 2004. That notice required the submission of new formal drawings adding prior art legends to Figures 1A and 1B. The notice of allowability did not include the notice of allowance and issue fee due form.

In response to the notice of allowability of December 17, 2004, the applicants submitted the revised formal drawings on March 16, 2005. The applicants also completed an issue fee transmittal (PTOL-85) form and paid the issue fee and publication fee on that same day. Copies of the stamped post cards identifying the USPTO's receipt of these documents are attached hereto as Appendices A & B. Receipt of those documents are reflected in the USPTO's on-line records.

On April 27, 2005, the USPTO issued a notice of allowance and issue fee due form. Because this fee was already received by the USPTO, this notice was mailed in error. The notice of allowance

of April 27, 2005 included what appears to be a copy of the notice of allowability of December 17, 2004. That notice of allowability requires the submission of corrected formal drawings. However, as noted above, the required corrected formal drawings were submitted in response to the first copy of the notice of allowability and are already reflected in the file.

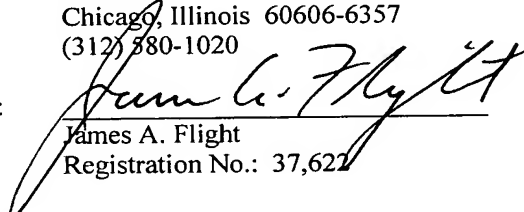
In view of the foregoing, no further action or fee is required from the applicants. Accordingly, the Office is respectfully requested to issue this application as soon as possible without further delay.

In this regard, it is respectfully submitted that the Office has further delayed this case with the issuance of the erroneous notice of allowance. Accordingly, the patent term should be adjusted to correct for this unnecessary delay occasioned by the USPTO's erroneous and duplicative efforts.

Respectfully submitted,

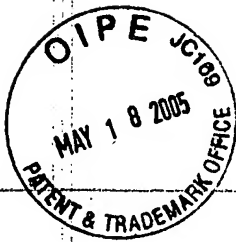
HANLEY, FLIGHT & ZIMMERMAN, LLC  
USPTO Customer Number 34431  
20 N. Wacker Drive  
Suite 4220  
Chicago, Illinois 60606-6357  
(312) 580-1020

By:

  
James A. Flight  
Registration No.: 37,622

May 16, 2005

## **APPENDIX A**



Docket No. 20059/PIA31207      March 16, 2005  
The Patent Office is hereby requested to acknowledge receipt of  
the following papers by stamping and returning this postcard.

JAF

U.S. Serial No. 10/750,245

Inventors: Seo

For: METHODS FOR FORMING A GATE IN A  
SEMICONDUCTOR DEVICE

Transmittal of formal drawings (1 sheet)

Annotated sheet (1 sheet)

Replacement sheets (3 sheets)

w/Certificate of mailing by first class mail dated March 16,  
2005.





## APPENDIX B



Docket No. 20059/PIA31207      March 16, 2005  
The Patent Office is hereby requested to acknowledge receipt of  
the following papers by stamping and returning this postcard.

JAF

U.S. Serial No. 10/750,245

Inventors: Seo

For: METHODS FOR FORMING A GATE IN A  
SEMICONDUCTOR DEVICE

Issue fee transmittal (1 sheet)

Our check #5466 in the amount of \$1700.00

w/Certificate of mailing by first class mail dated March 16,  
2005.

